USRP B200/B210 CR(cost-reduction)



Features:
RF coverage from 70 MHz – 2.5 GHz
Full GNU Radio/UHD compatible
USB 3.0 SuperSpeed interface
Micro-USB 3.0b USB 3.0 connector
Flexible rate 12 bit ADC/DAC

B200 CR
1 TX & 1 RX, Half or Full Duplex
Xilinx Spartan 6 XC6SLX75 FPGA
Up to 56 MHz of instantaneous bandwidth
USB Bus powered

B210 CR
2 TX & 2 RX, Half or Full Duplex
Xilinx Spartan 6 XC6SLX150 FPGA
Up to 56 MHz of instantaneous bandwidth 1T1R
Up to 56 MHz of instantaneous bandwidth 2T2R
USB Bus powered

Specification

Spec	Typi	Unit
USB Power Supply	5	v
ADC Sample Rate(max)	61.44	MS/s
ADC Resolution	12	bits
ADC wideband SFDR	78	dBc
DAC Sample Rate(max)	61.44	MS/s
DAC Resolution	12	bits
Frequency Accuarcy	+-2	ppm
Unlocked TCXO reference	+-75	ppb
Locked TCXO reference	<1	ppb

Spec	Typi	Unit
SSB/LO Suppression	35/50	dBc
2GHz	1	deg RMS
Output Power	>9	dBm
IIP3(@ tpy NF)	-20	dBm
RX NF	<8	dB
Dimension	9.7*15.1*1.5	cm
Weight	350	g

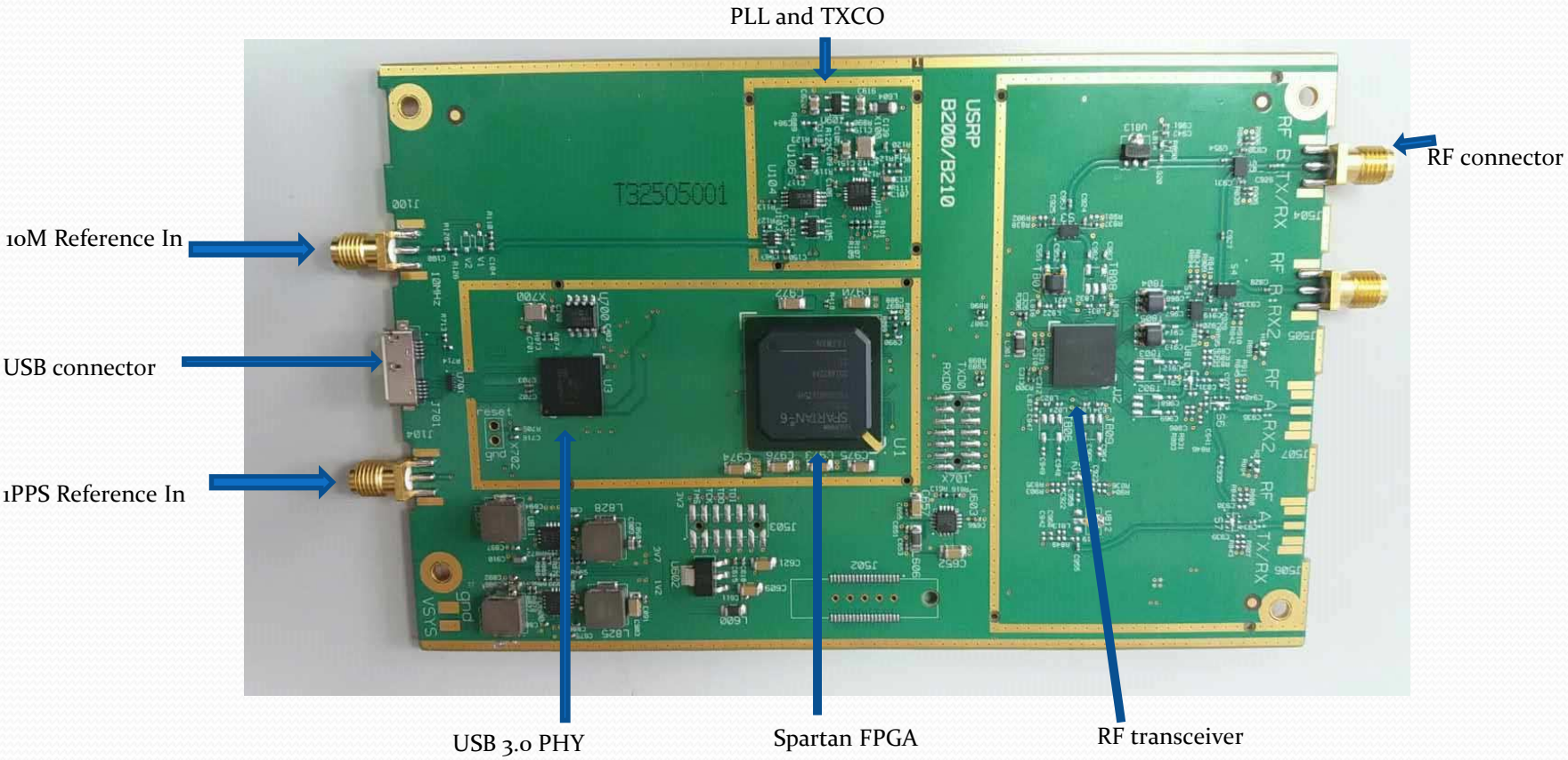
Compare between B200/B210 CR and B200/B210

Spec	B200	B200 CR
Frequency	70~6GHz	70~2.5GHz
External DC connector	Y	N*
On board GPSDO socket	Y	N

*

- USB power supply: 5w max
- B200 power consumption: 3w max
- B210 power consumption: 4w max

B200/B210 CR description



SDR platform roadmap



B200/210 CR

Features:
RF coverage from 70 MHz – 2.5 GHz
2T2R
Full GNU Radio/UHD compatible
USB 3.0 interface
Max 2T2R@ 30.72MS/s
Xilinx Spartan6 FPGA
Board only



NG1 SDR(Next Generation)
* (Demo picture)

Features:
RF coverage from 70 MHz – 6 GHz
2T2R and 2*Feedback Receiver
Full GNU Radio compatible, modified UHD support
2*10GE(SFP), 1*1GE(RJ45) interface
Max 2T2R@ 245.76MS/s
Xilinx K7 160T FPGA
Optional standalone mode
12v DC power supply with POE support
Ultra small box with Fan cooling

MP	ES	GA	MP
Jan 2017	Oct 2017	Dec 2017	Feb 2018

MP: mass production.
ES: engineering sample ready. Ready for offering.
GA: general available. Ready for trial.